

LMX Tester and Programming Interface

Revision History	
Revision	Notes

DIMENSIONS:

- Rectangular shape with height minimized (SMA spacing + board stand offs)
- Final PCB thickness 62 mil +/- 10% ****

STACKUP:

- Layer 1: Device layer, Power/GPIO Jumper/Switches, RF microstrip from DUT to SMA, USB connector, Silkscreens + Labeling
===== FR4: 8 mil
- Layer 2: Ground Plane
===== FR4: 38 mil
- Layer 3: Split Power planes for USB circuitry and DUT circuitry
===== FR4: 8
- Layer 4: USB circuitry

Controlled Impedance Traces

- TOP: 13 mil traces to be 50 ohm Zo +/- 5% reference to L2

HARDWARE AND MARKINGS

LAYOUT NOTE:
PLace 4 standoffs at corners of board.



PCB
LOGO
Pb-Free Symbol

PCB
LOGO
FCC disclaimer

PCB
LOGO
Texas Instruments

Label Table

Variant	Label Text
001	LMX Tester

PCB Number: SV601349
PCB Rev: A

ASSEMBLY NOTES

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5
Assembly Note
Default Shunt settings: SH1_2-3 means short Pins 2-3of J2 jumper.

ZZ6
Assembly Note
Default Shunt settings: SH2_2-3 means short Pins 2-3 of J3 jumper.

ZZ7
Assembly Note
Default Shunt settings: SH3_2-3 means short Pins 2-3 of OE jumper.

ZZ8
Assembly Note
Default Shunt settings: SH4_2-1 means short Pins 1-2 of OS jumper.

ZZ9
Assembly Note
Default Shunt settings: SH5_2-1 means short Pins 1-2 of FS0 jumper.

ZZ10
Assembly Note
Default Shunt settings: SH6_2-1 means short Pins 1-2 of FS1 jumper.

ZZ11
Assembly Note
Default Shunt settings: SH7_2-3 means short Pins 2-3 of J7 jumper.

ZZ12
Assembly Note
Default Shunt settings: SH7_2-3 means short Pins 2-3 of J8 jumper.



